Declaration

I hereby declare that the thesis entitled Scale Length Determination, Complete 2-D Potential Modelling and Enhancement of Some Performance Parameters of Junctionless Transistors submitted by me to the Tezpur University in partial fulfillment of requirement for the award of the degree of Doctor of Philosophy in the discipline of Electronics and Communication Engineering is original to the best of my knowledge and belief. The thesis has not been submitted anywhere else in part or in full for award of any other degree.

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