
List of Publications Based on the Thesis Works

- [I] **Boruah, K.** and Dutta, J. C. An improved generalized DNA computing model to simulate logic functions and combinational circuits. *International Journal of Information Technology*, 10(3): 379-390, 2018, Springer, DOI: 10.1007/s41870-018-0110-7.
- [II] **Boruah, K.**, and Dutta, J. C. Algorithm to Simulate a Chemically Induced DNA Logic Gate and Boolean Circuit. *Current Trends in Biotechnology and Pharmacy*, 11(2): 160-166, 2017.
- [III] **Boruah, K.** and Dutta, J. C. A DNA computing algorithm of realization of DNA Boolean logic based on micro-cantilever deflection, *International Journal of Pharmaceutical Sciences Review and Research*, 42(1): 111-116, 2017.
- [IV] **Boruah, K.** and Dutta, J. C. Realization of Boolean Circuit using DNA computing, *International Journal of Control Theory and Applications*, 9(11): 281-287, 2016.
- [V] **Boruah, K.** and Dutta, J. C. Development of a DNA computing model for Boolean Circuit. In *2nd International Conference on Advances in Electrical,*

Electronics, Information, Communication and Bio-Informatics (AEEICB), pages 301-304, Chennai, India, 27-28 February 2016, IEEE.

- [VI] **Boruah, K.** and Dutta, J. C. DNA Computing Models for Boolean Circuits and Logic Gates. In *IEEE International Conference Computational Intelligence & Communication Technology (CICT)*, pages 529-533, Ghaziabad, India, 13-14 February 2015, IEEE.
- [VII] **Boruah, K.** and Dutta, J. C. Twenty years of DNA computing: From complex combinatorial problems to the Boolean circuits. In *International Conference on Electronic Design, Computer Networks & Automated Verification (EDCAV)*, pages 52-57, Shillong, India, 29-30 January 2015, IEEE.
- [VIII] **Boruah, K.** and Dutta, J. C. A Model to Demonstrate the Universality of DNA-NAND Gate. *Advances in Electronics, Communication and Computing*, vol 443, 67-76, 2018, Springer, DOI: 10.1007/978-981-10-4765-7_8.
- [IX] **Boruah, K.** and Dutta, J. C. A DNA computing algorithm to simulate Logic Gate. *ACM-W "Regional Celebration of Women in Computing, East and North-East"*, 2-3 November 2015, Department of CSE, IIT Guwahati, ACM India.